





## UTILITY PATENT APPLICATION UNDER 37 CFR 1.53(b)

Box PATENT APPLICATION Commissioner for Patents Washington, DC 20231 Sir:



Transmitted herewith for filing is the patent application of:

INVENTOR: Yukikazu MATSUO, Yoshihiro NAGURA

FOR: TESTING DEVICE OF SEMICONDUCTOR INTEGRATED CIRCUIT AND

TEST METHOD THEREFOR

$\boxtimes$	23 pages of specification, claims, abstract.						
$\boxtimes$	Declaration and Power of Attorney.						
$\boxtimes$	Priority Claimed.						
$\boxtimes$	Certified copy of Japanese Patent Application Number 2001-224899						
	9 sheets of formal drawing.						
$\overline{\boxtimes}$	An assignment of the invention to Mitsubishi Denki Kabushiki Kaisha and Ryoden						
	Semiconductor System Engineering Corporation						
	and the assignment recordation fee.						
	An associate power of attorney.						
	A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.						
$\overline{\boxtimes}$	Information Disclosure Statement, Form PTO-1449 and reference.						
$\overline{\square}$	Return Receipt Postcard						
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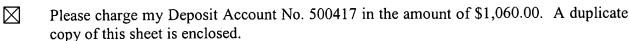
The filing fee has been calculated as shown below:

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· · · · · · · · · · · · · · · · · · ·	CLAIMS		CLAIMS	RATE	AMOUNT
Total Claims	8	-20	0	\$18.00	\$0.00
Independent Claims	3	-3	0	\$84.00	\$0.00
	\$280.00				
	\$740.00				
	\$1,020.00				
	\$0.00				
	t & Recording Fee	\$40.00			
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The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 500417. A duplicate copy is enclosed.

Any additional filing fees required under 37 CFR 1.16.

The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 500417. A duplicate copy of this sheet is enclosed.

Any patent application processing fees under 37 CFR 1.17.

Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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